

Fig.3(a)

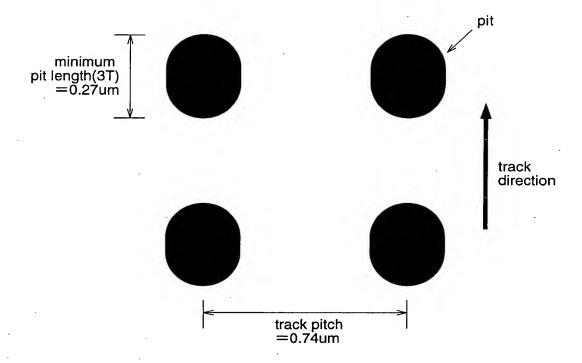
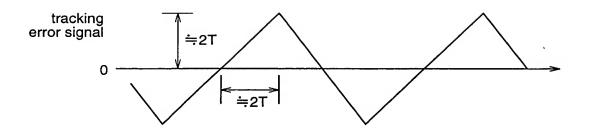


Fig.3(b)



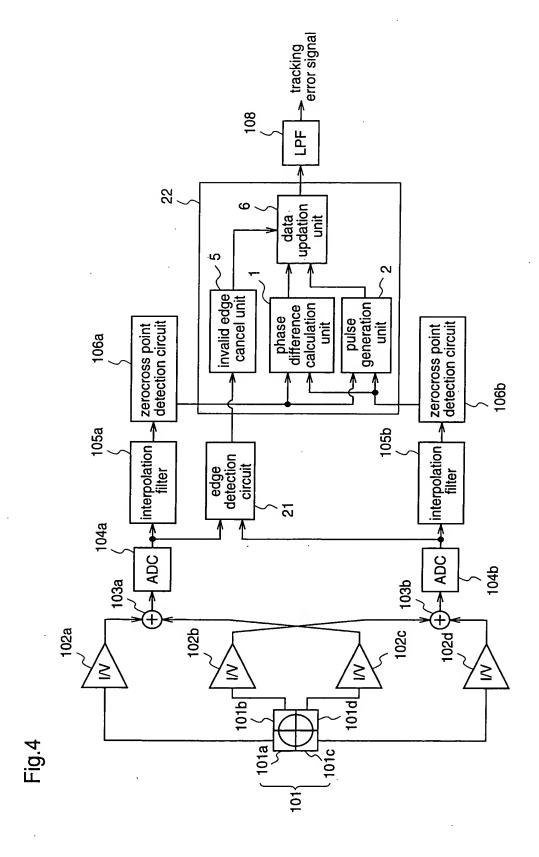


Fig.5(a)

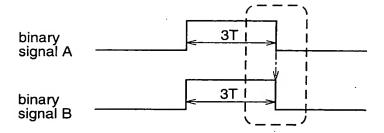


Fig.5(b)

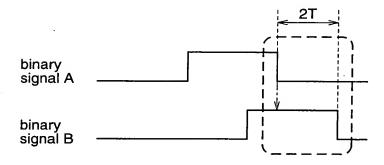
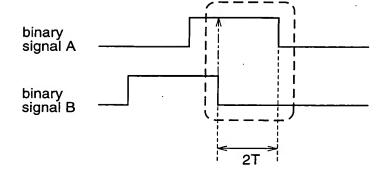
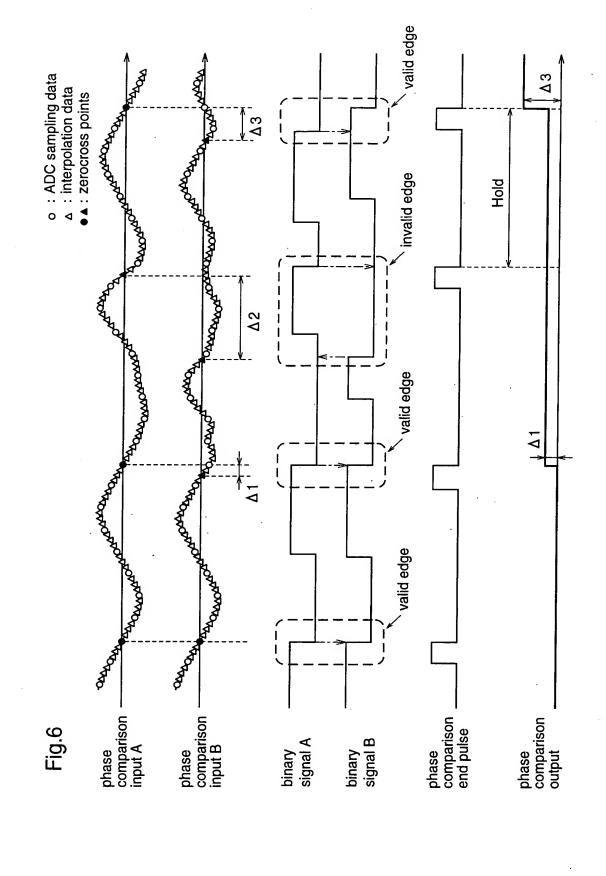


Fig.5(c)





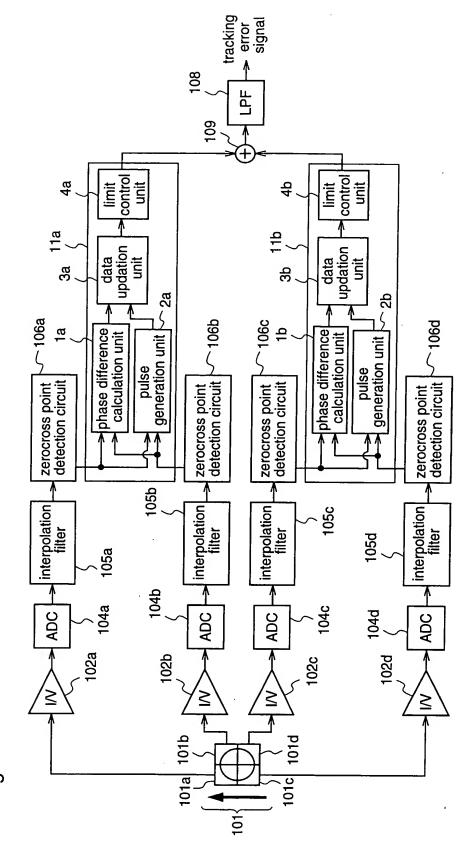


Fig.7

Fig.8(a)

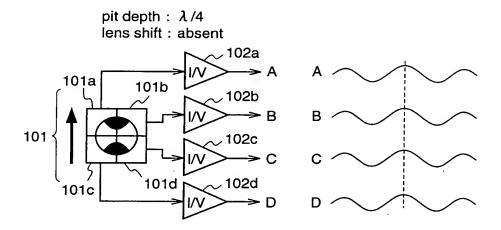


Fig.8(b)

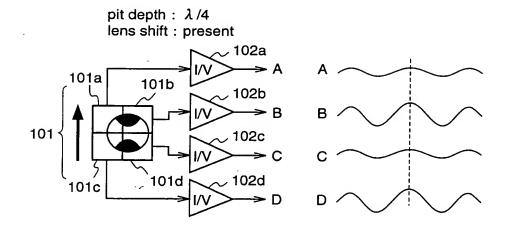


Fig.8(c)

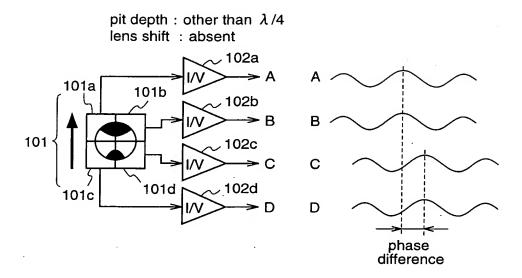
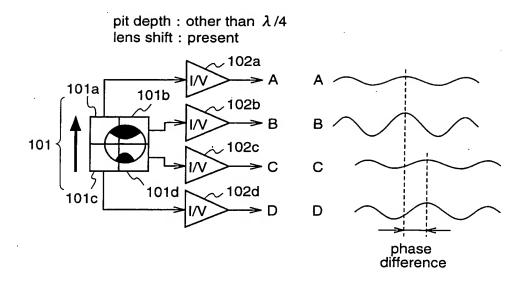
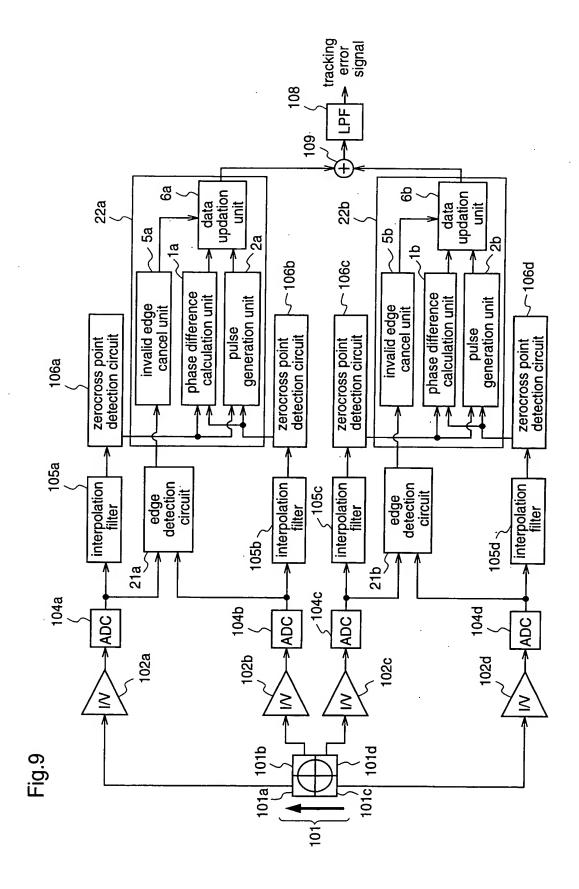


Fig.8(d)





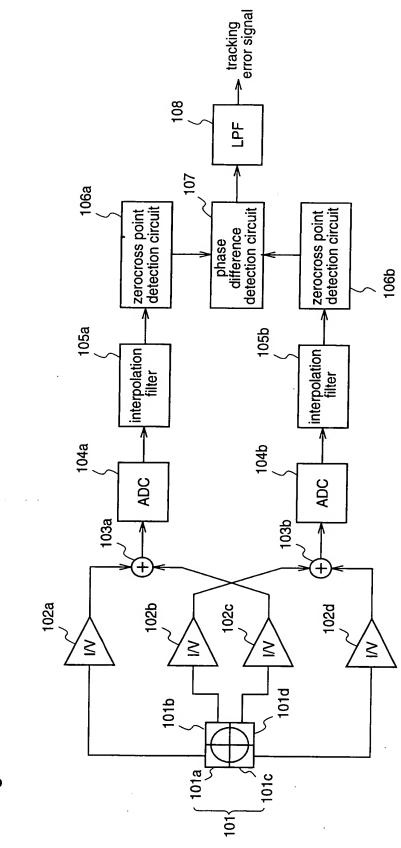
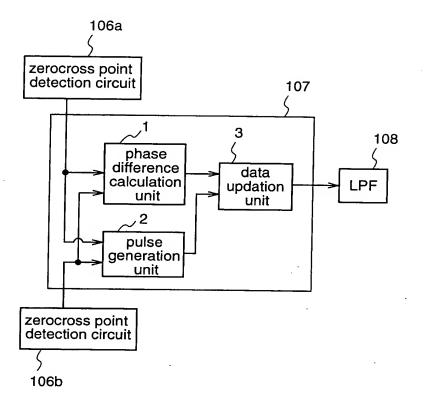


Fig.10 Prior Art

Fig.11 Prior Art



o : ADC sampling data△ : interpolation data◆ * : zerocross points ^ \\ **V**2√ A A A Fig.12 Prior Art phase comparison input A phase comparison input B phase comparison end pulse phase comparison output

₹2

Δ3

Fig.13 Prior Art

